



XA-9582

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Yoshihiro IKEDA et al.

Appln. No.: 09/987,884

Group Art Unit: 2826

Filed: November 16, 2001

Examiner: K. Quinto

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
MANUFACTURING METHOD THEREOF

* * *

AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

09/26/2002 JILLIAN 0000004 501163 09987884 Responding to the Office Action mailed August 21, 2002

01 FC:103

54.00 CH
please amend the above-identified application as indicated
below.

IN THE CLAIMS:

Please add the following claims:

A